


<b>Notice of Allowability</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/645,408	PRADHAN ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	Patricia T Nguyen	2817	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☐ This communication is responsive to \_\_\_\_\_.
2. ☒ The allowed claim(s) is/are 1-7.
3. ☒ The drawings filed on 05 April 2004 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- |   |  |
|---|--|
| 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)   | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)            |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)  | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____. |
| 3. <input checked="" type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br>Paper No./Mail Date <u>3/19/2004</u> | 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment                    |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material                                | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance   |
|   | 9. <input type="checkbox"/> Other _____.   |

### EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

Claim 1, line 25, "PMOS connected to the source of the source of the PMOS in the first current path" has been changed to -- PMOS connected to the source of the PMOS in the second current path --.

Claim 1, line 26, "in the first current path for a first current" has been changed to -- in the first current path formed a first current --.

Claim 1, line 28, "first current path, wherein" has been changed to -- second current path, wherein --.

Claim 1, line 32, "PMOS connected to the source of the source of the PMOS in the second current path" has been changed to -- PMOS connected to the source of the PMOS in the first current path --.

Claim 1, line 35, "NMOS in the second current path" has been changed to -- NMOS in the first current path --.

Claim 1, line 35, "NMOS in the second current path" has been changed to -- NMOS in the first current path --.

Claim 1, line 41, "PMOS transistors in the first" has been changed to -- PMOS transistors in the second --.

Claim 1, lines 44 and 45, "NMOS transistors in the second current path" has been changed to -- NMOS transistors in the first current path --.

Claim 4, line 11, "the second PMOS pair, wherein" has been changed to -- the second PMOS pair forming an intermediate differential output, wherein -- in order to define the antecedent for "the intermediate differential output" in lines 28 and 29 of claim 4.

Claim 6, line 13, "a value higher that" has been changed to -- a value higher than --.

The following is an examiner's statement of reasons for allowance: prior art, U.S. Patent # 6,433,637 B1 of Sauer does not teach nor render obvious a low voltage differential amplifier architecture with differential input and output signals that has, in combination with other limitations, all the gates of the PMOS and NMOS transistors in the first and the second current paths and in the first and second biasing trees all are connected together and the third current source and the fourth current source are equal to each other and each is of a higher value than the second current source, the fifth current source and the sixth current source are equal to each other and each is of a higher value than the first current source; neither does it have the gates of the first and second stacked transistors are connected together and the four current sources are each equal to each other and each is of a value higher than one half that of each of the first current sources.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### ***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. U.S. Patents # 6,433,637 B1, # 6,275,073 B1, # 6,313,696 B1, # 5,894,245 contain some limitations of the claimed invention.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Patricia T Nguyen whose telephone number is (703) 308-1927. The examiner can normally be reached on 6:30 AM - 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on 703-309-4940. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PTN

October 29, 2004



**PATRICIA NGUYEN**  
**PRIMARY EXAMINER**